

AMENDMENT TO THE TITLE OF THE INVENTION

Please replace the original title of the invention with the following rewritten title.

“INTEGRATED CIRCUIT AND ELECTRIC DEVICE FOR AVOIDING A LATENCY
TIME CAUSED BY ACCESS CONTENTION”

AMENDMENTS TO THE SPECIFICATION AND ABSTRACT

In the specification, page 3, paragraph [0019], please amend the paragraph as follows:

With this structure, since the second processing unit, which performs at least one of data processing and calculation in a larger amount than the first processing unit, uses the second memory, the second processing unit is released from contention of access to the first memory. That is, the second processing unit can process without latency time caused by the contention, and a probability that the integrated circuit can complete processes to be done in real time increases.

In the specification, page 4, paragraph [0022], please amend the paragraph as follows:

With this structure, the integrated circuit can be used as a decoder that decodes compressed multimedia data. The decoder can expand compressed video signals without latency time caused by contention of access, although burden of expanding the compressed video signals is heavy. ~~Probability~~ Accordingly, a probability that the integrated circuit can complete processes to be done in real time can ~~be retained~~ remain high.

In the specification, page 4, paragraph [0024], please amend the paragraph as follows:

With this structure, the integrated circuit can be used as an encoder that compresses multimedia data. The encoder can compress the video signals without latency time caused by contention of access, although burden of compressing the video signals is heavy. ~~Probability~~ Accordingly, a probability that the integrated circuit can complete processes to be done in real time can ~~be retained~~ remain high.

In the specification, page 7, after paragraph [0039], in the title, please amend as follows:

DETAILED DESCRIPTION OF THE ~~PREFERRED EMBODIMENTS~~INVENTION

In the specification, page 9, paragraph [0053], please amend the paragraph as follows:

The video processor 105 stores data in the local memory 110 and ~~read~~reads data from the local memory 110. The data is composed of reference image data generated while the video signals are processed (expanded or compressed), data of the computer graphics images, and data of instructions accompanying thereto. The data may be transferred with or without using a DMAC (Direct Memory Access Controller).